ACKNOWLEDGEMENT RECEIPT

Electronic Version 1.1

Stylesheet Version v1.1.1

Title of Invention

EFS ID:

Embedded Processor with Watchdog Timer for Programmable Logic

Submision Type: Utility Patent Filing

Application Number:

10/711137

67502

Server Response:

Confirmation Message Code ISVR1 Submission was successfully submitted -Even if Informational or Warning Messages appear below, please do not resubmit this application ICON1 5136 USPTOEFSNot Eer assistance with e-filing a patent application, contact the Patent Electronic Business Center: Toll-Free Number:1(866) 217-9197 Website: http://www.uspto.gov/ebc/ Filename= N/A BusinessRule= The access IGEN77 code is not included which allows for automated processing of fees from the deposit account. #Supporting Msg:Either

fucntion call.

<us-access-code> not found in fee-xml OR actual access code not provided in the

First Named Applicant: Andrew Crosland
Attorney Docket Number: 15114-053510

Timestamp: 2004-08-27 00:39:10 EDT

From: us

File Listing:

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